

NOV 29 2005

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<b>To:</b>	Examiner Matthew E. WARREN Art Unit: 2815	<b>From:</b>	Leslie S. Szivos
<b>Fax:</b>	571-273-8300	<b>Pages:</b>	9 pages including fax cover sheet
<b>Phone:</b>		<b>Date:</b>	11/29/2005
<b>Re:</b>	U.S. Patent Appln. No. 10/787,002 Docket: YOR920030635US1 (17343) Confirmation No. 3930	<b>CC:</b>	

**RESPONSE TO OFFICE ACTION**

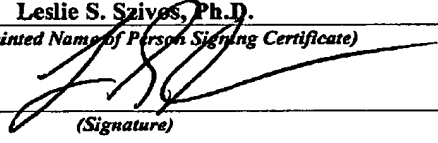
Please see the following documents:

1. Amendment Transmittal (in duplicate)
2. Authorization to charge IBM deposit account
3. Response under 37 C.F.R. § 1.111
4. Certificate of transmission by facsimile dated November 29, 2005


If you have any questions concerning this facsimile please contact Vivian Henriquez at 516-742-4343.  
Thank you.

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NOV 29 2005


<b>CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)</b> Applicant(s): <b>Herbert L. Ho et al.</b>			<b>Docket No.</b> <b>YOR920030635US1 (17343)</b>
<b>Application No.</b> <b>10/787,002</b>	<b>Filing Date</b> <b>February 25, 2004</b>	<b>Examiner</b> <b>Matthew E. Warren</b>	<b>Group Art Unit</b> <b>2815</b>
<b>Invention: ULTRA-THIN SOI VERTICAL BIPOLAR TRANSISTORS WITH AN INVERSION COLLECTOR ON THIN-BURIED OXIDE (BOX) FOR LOW SUBSTRATE-BIAS OPERATION AND METHODS THEREOF</b>			
<p>I hereby certify that this <u>RESPONSE UNDER 37 C.F.R. § 1.111</u> (Identify type of correspondence) is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>571-273-8300</u> on <u>November 29, 2005</u> (Date)</p> <p style="text-align: center;"><u>Leslie S. Szives, Ph.D.</u> (Typed or Printed Name of Person Signing Certificate)  (Signature)</p> <p style="text-align: center;"><b>Note: Each paper must have its own certificate of mailing.</b></p>			

P18/REV02

<b>TRANSMITTAL LETTER</b> (General - Patent Pending)				Docket No. <b>YOR920030635US1 (17343)</b>	
In Re Application Of: <b>Herbert L. Ho et al.</b>					
Application No. <b>10/787,002</b>	Filing Date <b>February 25, 2004</b>	Examiner <b>Matthew E. Warren</b>	Customer No. <b>23389</b>	Group Art Unit <b>2815</b>	Confirmation No. <b>3930</b>
Title: <b>ULTRA-THIN SOI VERTICAL BIPOLAR TRANSISTORS WITH AN INVERSION COLLECTOR ON THIN-BURIED OXIDE (BOX) FOR LOW SUBSTRATE-BIAS OPERATION AND METHODS THEREOF</b>					
<b><u>COMMISSIONER FOR PATENTS:</u></b>					
Transmitted herewith is: <b>RESPONSE UNDER 37 C.F.R. § 1.111</b>					
in the above identified application.					
<input checked="" type="checkbox"/> No additional fee is required.					
<input type="checkbox"/> A check in the amount of _____ is attached.					
<input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. <b>50-0510/IBM</b> as described below.					
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<input checked="" type="checkbox"/> Charge any additional fee required.					
<input type="checkbox"/> Payment by credit card. Form PTO-2038 is attached.					
<b>WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.</b>					
 Signature			Dated: <b>November 29, 2005</b>		
<b>Leslie S. Szivos, Ph.D.</b> <b>Registration No. 39,394</b> <b>SCULLY, SCOTT, MURPHY &amp; PRESSER</b> <b>400 Garden City Plaza - Suite 300</b> <b>Garden City, New York 11530</b>					
CC:			<div style="text-align: center;">I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on _____ (Date)  _____ Signature of Person Mailing Correspondence  _____ Typed or Printed Name of Person Mailing Correspondence</div>		

P16A/REV03

NOV 29 2005

<b>TRANSMITTAL LETTER</b> <b>(General - Patent Pending)</b>				Docket No. <b>YOR920030635US1 (17343)</b>	
In Re Application Of: <b>Herbert L. Ho et al.</b>					
Application No. <b>10/787,002</b>	Filing Date <b>February 25, 2004</b>	Examiner <b>Matthew E. Warren</b>	Customer No. <b>23389</b>	Group Art Unit <b>2815</b>	Confirmation No. <b>3930</b>
Title: <b>ULTRA-THIN SOI VERTICAL BIPOLAR TRANSISTORS WITH AN INVERSION COLLECTOR ON THIN-BURIED OXIDE (BOX) FOR LOW SUBSTRATE-BIAS OPERATION AND METHODS THEREOF</b>					
<u>COMMISSIONER FOR PATENTS:</u>					
Transmitted herewith is:  <b>RESPONSE UNDER 37 C.F.R. § 1.111</b>					
in the above identified application.					
<input checked="" type="checkbox"/> No additional fee is required.					
<input type="checkbox"/> A check in the amount of _____ is attached.					
<input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. <b>50-0510/IBM</b> as described below.					
<input type="checkbox"/> Charge the amount of _____					
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<input checked="" type="checkbox"/> Charge any additional fee required.					
<input type="checkbox"/> Payment by credit card. Form PTO-2038 is attached.					
<b>WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.</b>					
 <i>Signature</i>			Dated: <b>November 29, 2005</b>		
Leslie S. Szivos, Ph.D. Registration No. 39,394 <b>SCULLY, SCOTT, MURPHY &amp; PRESSER</b> 400 Garden City Plaza - Suite 300 Garden City, New York 11530			<div style="border: 1px solid black; padding: 5px;"><p>I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on _____</p><p>(Date)</p><p>_____ <i>Signature of Person Mailing Correspondence</i></p><p>_____ <i>Typed or Printed Name of Person Mailing Correspondence</i></p></div>		
CC:					

P16A/REV03

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant(s):** Herbert L. Ho, et al.      **Examiner:** Warren, Matthew E.  
**Serial No:** 10/787,002      **Art Unit:** 2815  
**Filed:** February 25, 2004      **Docket:** YOR920030635US1 (17343)  
**Dated:** November 29, 2005  
**For:** ULTRA-THIN SOI VERTICAL BIPOLAR TRANSISTORS WITH  
AN INVERSION COLLECTOR ON THIN-BURIED OXIDE (BOX)  
FOR LOW SUBSTRATE-BIAS OPERATION AND METHODS  
THEREOF

**Confirmation No. 3930**

Mail Stop Amendment  
Commissioner for Patents  
United States Patent Office  
Alexandria, VA 22313-1450

**RESPONSE UNDER 37 C.F.R. § 1.111**

Sir:


In response to the Office Action dated August 29, 2005, applicants submit the following remarks for entry of record in the above-identified patent application.

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**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that this document is being filed in the United States Patent and Trademark Office on the date shown below via facsimile transmission to Mail Stop Amendment, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 at United States Patent and Trademark Office facsimile transmission number (571) 273-8300.

Dated: November 29, 2005

  
\_\_\_\_\_  
Leslie S. Szivos, Ph.D.

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